FORM PTO-1449					Attorney's Docket No.		Serial No. O To Be Assigned a		
(REV. 6-89)		Patent and Trademark Office			19570-053	34	To Be Assigned a		
INF	ORIV	ATION DISCLOSURE CITATION			Applicant Sang-Hyun Lee and Deog-Kyoon Jeong				
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TAU	H Maneatis, John G., "Low-Jitter Process Independent DLL and PLL Based on Self-Biased Techniques," IEEE Journal of Solid-State Circuits, pp.1723-1732, No.11, November 1996								
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PTO-1449 REV: 02/01

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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of

COMPLETE IF KNOWN						
Application Number	09/943,029					
Confirmation Number	9521					
Filing Date	August 29, 2001					
First Named Inventor	Sang-Hyun Lee					
Group Art Unit	2631					
Examiner Name	Not Assigned					
Attorney Docket No.	59472-8073.US01					

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